

Department: **Electronics Engineering**

Semester: I (Batch 2024-26)

M.Tech in “**Electronics and Communication Engineering**”

Specialization in “**Radar and Communication**”

	0900-1000	1000-1100	1100-1200	1200-1300	1300-1330	1400-1500	1500-1600	1600-1700
Monday	EE624	EE624	-----	EE602	Lunch Break	EE603	EE603	AM607
Tuesday	EE601	EE601	-----	EE603		EE601	RM	AM607
Wednesday	EE604	EE604	EE602	EE602		EE624 Lab	EE624 Lab	AM607
Thursday	EE601 Lab	EE601 Lab	-----	EE604		-----	RM	AM607
Friday	EE604 Lab	EE604 Lab	EE602 Lab	EE602 Lab		EE603 Lab	EE603 Lab	EE624

S. No	Course Code	Course	Name of Instructor	Room
1	EE601	Microwave Engineering	Prof. K P Ray & Dr. R K Singh	208
2	EE602	Digital Signal Processing	Dr. R R Sharma	208
3	EE603	Wireless and Mobile Communication	Dr. R K Singh	208
4	EE604	Radar System Design	Prof. K P Ray	208
5	EE624	Digital System Design using FPGA	Prof. A A Bazil Raj	208
6	AM607	Mathematics for Engineers	Faculty of Dept. of AM	To be decided
7	PGC601	RM and IPR	Prof. Balasubramanian K.	Online

Rajesh K Singh

Dr. Rajesh K. Singh (Programme Coordinator)

K P Ray
3/7/24

Prof. K P Ray (HoD EE)

Department: **Electronics Engineering**

Semester: I (**Batch 2024-26**)

M.Tech in “**Electronics and Communication Engineering**”

Specialization in “**Signal Processing and Communication**”

	0900-1000	1000-1100	1100-1200	1200-1300	1300-1330	1400-1500	1500-1600	1600-1700
Monday	EE624	EE624	-----	EE602	Lunch Break	EE603	EE603	AM607
Tuesday	EE601	EE601	-----	EE603		EE601	RM	AM607
Wednesday	EE604	EE604	EE602	EE602		EE624 Lab	EE624 Lab	AM607
Thursday	EE601 Lab	EE601 Lab	-----	EE604		-----	RM	AM607
Friday	EE604 Lab	EE604 Lab	EE602 Lab	EE602 Lab		EE603 Lab	EE603 Lab	EE624

S. No	Course Code	Course	Name of Instructor	Room
1	EE601	Microwave Engineering	Prof. K P Ray & Dr. R K Singh	208
2	EE602	Digital Signal Processing	Dr. R R Sharma	208
3	EE603	Wireless and Mobile Communication	Dr. R K Singh	208
4	EE604	Radar System Design	Prof. K P Ray	208
5	EE624	Digital System Design using FPGA	Prof. A A Bazil Raj	208
6	AM607	Mathematics for Engineers	Faculty of Dept. of AM	To be decided
7	PGC601	RM and IPR	Prof. Balasubramanian	Online

Rajesh K Singh

Dr. Rajesh K. Singh (Programme Coordinator)

K P Ray
3/7/24
Prof. K P Ray (HoD EE)

Department: **Electronics Engineering**

Semester: I (Batch 2024-26)

M.Tech in “**Electronics and Communication Engineering**”

Specialization in “**Defence Electronics Systems**”

	0900-1000	1000-1100	1100-1200	1200-1300	1300-1330	1400-1500	1500-1600	1600-1700
Monday	EE624	EE624	-----	EE602	Lunch Break	EE603	EE603	AM607
Tuesday	EE601	EE601	-----	EE603		EE601	RM	AM607
Wednesday	EE604	EE604	EE602	EE602		EE624 Lab	EE624 Lab	AM607
Thursday	EE601 Lab	EE601 Lab	-----	EE604		-----	RM	AM607
Friday	EE604 Lab	EE604 Lab	EE602 Lab	EE602 Lab		EE603 Lab	EE603 Lab	EE624

S. No	Course Code	Course	Name of Instructor	Room
1	EE601	Microwave Engineering	Prof. K P Ray & Dr. R K Singh	208
2	EE602	Digital Signal Processing	Dr. R R Sharma	208
3	EE603	Wireless and Mobile Communication	Dr. R K Singh	208
4	EE604	Radar System Design	Prof. K P Ray	208
5	EE624	Digital System Design using FPGA	Prof. A A Bazil Raj	208
6	AM607	Mathematics for Engineers	Faculty of Dept. of AM	To be decided
7	PGC601	RM and IPR	Prof. Balasubramanian	Online

Rajesh K Singh

Dr. Rajesh K. Singh (Programme Coordinator)

K P Ray
3/7/24

Prof. K P Ray (HoD EE)

Department: **Electronics Engineering**

Semester: I

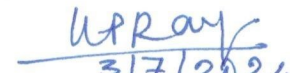
M.Tech in “**Electronics and Communication Engineering**”

Specialization in “**VLSI and Embedded Systems**”

	0900-1000	1000-1100	1100-1200	1200-1300	1300-1330	1400-1500	1500-1600	1600-1700
Monday	EE624	EE624	EE621	EE602	Lunch Break	EE650	EE650	AM607
Tuesday	EE622	----	EE650 Lab	EE650 Lab		EE650	RM	AM607
Wednesday	----	EE621	EE602	EE602		EE624	EE624	AM607
Thursday	EE621	----	EE622	EE622		-----	RM	AM607
Friday	EE621 Lab	EE621 Lab	EE602 Lab	EE602 Lab		EE622 Lab	EE622 Lab	EE624

S. No	Course Code	Course	Name of Instructor	Room
1	EE602	Digital Signal Processing	Dr. R R Sharma	208
2	EE621	Digital IC Design	Mr. Abhilash M T	112
3	EE622	RF IC Design	Dr. B C Mech/ Dr. R R Sharma	112
4	EE624	Digital System Design using FPGA	Dr. A Bazil Raj	112
5	EE650	Semiconductor devices for high speed and high power applications	Dr. B. C. Mech	112
6	AM607	Mathematics for Engineers	Faculty of Dept. of AM	To be decided
7	PGC601	RM and IPR	Prof. Balasubramanian	Online


3/7/2024
Dr. B C Mech (Programme Coordinator)


3/7/2024
Prof. K P Ray (HoD EE)